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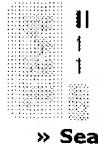
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Lin, M.-W.; Armstrong, J.R.; Frank, G.A.; Concha, L.;
Verilog HDL Conference and VHDL International Users Forum, 1998. IVC/VIUF Proceedings., 1998 International , 16-19 March 1998
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18 An automated design process for the CHAMP module

Patriquin, R.; Gurevich, I.;
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Horta, N.C.; Franca, J.E.;

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Gopal, R.; Prasad, R.;

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